

10/91/80

ISSUE CLASSIFICATION	
Class	Subclass

N

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. 12W SCANNED <u>TR4</u> Q.A. <u>AG</u>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/931710		711 710	307 508	2185 2181	Andrew Knoll
APPLICANTS	Lloyd Thorsbakken Larry Byers Michael Overley				
TITLE	Logical PCI bus				

PTO-20
12/88

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE Amount Due Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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Data Base

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